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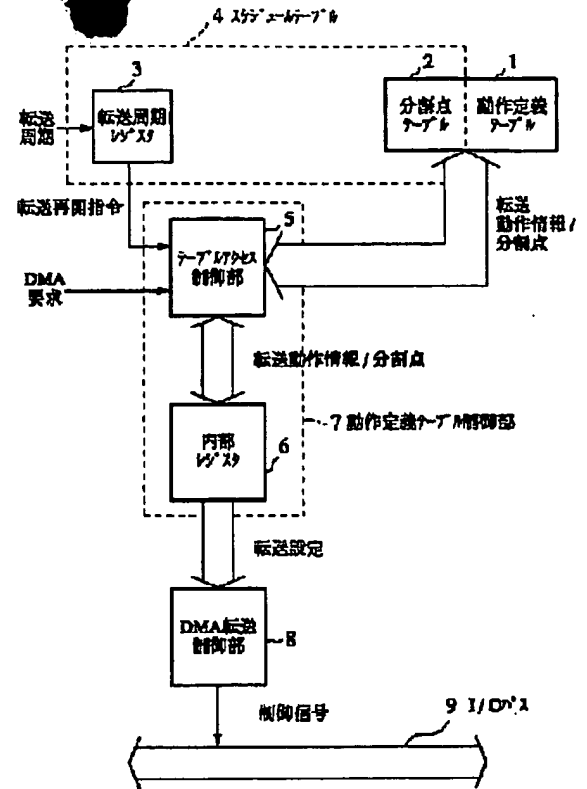
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TITLE : DMA CONTROLLER



ABSTRACT : PURPOSE: To improve the transfer efficiency by enabling DMA transfer and transferring data continuously in a divided transfer cycle under transfer conditions by single-time setting.

CONSTITUTION: This DMA controller is provided with an operation definition table 1 where transfer operation information is set and schedule management means 2, 3, and 4 where divided transfer cycles and transfer possible/impossible state data are sets, and the schedule management means send transfer restart commands in each divided transfer cycle; once a DMA request or transfer restart command is received, transfer operation information on some object data is stored in operation storage means 5 and 6 on the basis of the contents of the operation definition table and the transfer possible/impossible state data. A transfer operation setting means 7 sets the transfer operation information, a DMA transfer control means 8 controls a bus according to the set transfer operation information, and an operation update means 9 updates and stores transfer operation information on other object data in the operation storage means during the control over the bus according to the contents of the operation definition table and the transfer possible/impossible state data.

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